

Education

- The George Washington University Law School, LL.M., 2004
- Handong International Law School, 2003
- Yonsei University, M.S., 1998
- Kyungbuk National University, B.S., 1982



Jong Ho (Hank) Lee

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Background

Mr. Lee is a registered patent attorney. Mr. Lee's academic qualifications include a major in Electrical Engineering at the undergraduate level and Intellectual Property Laws at the graduate level. Mr. Lee has drafted and prosecuted patent applications for U.S. and international clients for 12 years at K&L Gates and BSKB prior to NSIP. In addition, Mr. Lee has 18 years of industry experience in the semiconductor field as engineer and patent manager of Hynix Semiconductor (currently SK Hynix), a global semiconductor company.

Admissions

- Washington, D.C. Bar
- New York State Bar
- USPTO

Practice

- Patent
- IP Enforcement
- Post-Grant and Interference

Based on substantial hands-on experience, Mr. Lee has developed an expertise in semiconductor memory devices, processes and assembly, digital circuit, IC design, and display technology fields. As a patent manager at Hynix, Mr. Lee has performed numerous patent invalidity and non-infringement analyses for the offensive and defensive patents involved in the patent licensing and litigation matters.

Language

- Korean
- Japanese
- Chinese (Mandarin)
- English

Technology

- Semiconductor devices, fabrications and assembly
- LCD/LED/OLED display devices
- Digital circuits and IC memory designs
- Image processing devices, Information processing devices
- Telecommunication, Computer hardware
- Jet engine, Automobile components, Mechanical devices